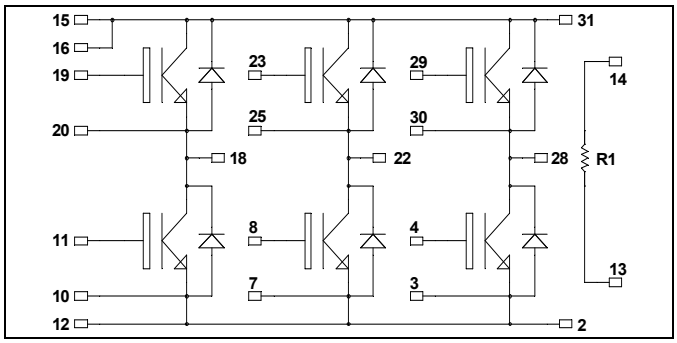
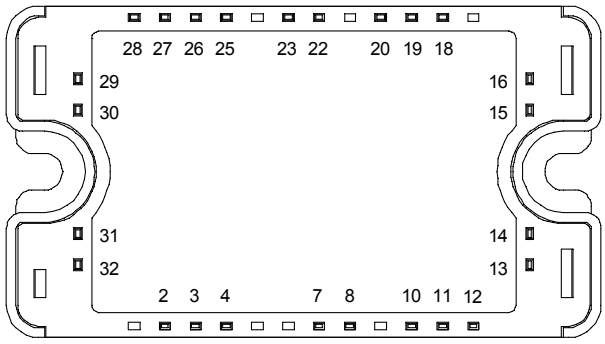
3 Phase bridge
NPT IGBT Power Module

V_{CES} = 1200V
I_C = 25A @ Tc = 80°C



It is recommended to connect a decoupling capacitor between pins 31 & 2 to reduce switching overvoltages, if DC Power is connected between pins 15, 16 & 12.
Pins 15 & 16 must be shorted together.



Application

- Motor control

Features

- Non Punch Through (NPT) Fast IGBT
  - Low voltage drop
  - Low tail current
  - Switching frequency up to 50 kHz
  - Soft recovery parallel diodes
  - Low diode VF
  - Low leakage current
  - RBSOA and SCSOA rated
- Kelvin emitter for easy drive
- Very low stray inductance
- High level of integration
- Internal thermistor for temperature monitoring

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS compliant

Absolute maximum ratings

Symbol	*Parameter*		*Max ratings*	*Unit*
V_{CES}	Collector - Emitter Breakdown Voltage		1200	V
I_C	Continuous Collector Current	T_C = 25°C	40	A
		T_C = 80°C	25	
I_{CM}	Pulsed Collector Current	T_C = 25°C	100	
V_{GE}	Gate – Emitter Voltage		±20	V
P_D	Maximum Power Dissipation	T_C = 25°C	208	W
RBSOA	Reverse Bias Safe Operating Area	T_J = 125°C	50A@1150V	

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

All ratings @ T_j = 25°C unless otherwise specified

Electrical Characteristics

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
I_{CES}	Zero Gate Voltage Collector Current	V_{GE} = 0V V_{CE} = 1200V	T_j = 25°C			250	µA
			T_j = 125°C			500	
$V_{CE(sat)}$	Collector Emitter saturation Voltage	V_{GE} =15V I_C = 25A	T_j = 25°C	2.5	3.2	3.7	V
			T_j = 125°C		4.0		
$V_{GE(th)}$	Gate Threshold Voltage	V_{GE} = V_{CE} , I_C = 1mA		4		6	V
I_{GES}	Gate – Emitter Leakage Current	V_{GE} = 20V, V_{CE} = 0V				400	nA

Dynamic Characteristics

Symbol	*Characteristic*	*Test Conditions*	*Min*	*Typ*	*Max*	*Unit*
C_{ies}	Input Capacitance	V_{GE} = 0V V_{CE} = 25V f = 1MHz		1650		pF
C_{oes}	Output Capacitance			250		
C_{res}	Reverse Transfer Capacitance			110		
Q_g	Total gate Charge	V_{GE} = 15V V_{Bus} = 600V I_C =25A		160		nC
Q_{ge}	Gate – Emitter Charge			10		
Q_{gc}	Gate – Collector Charge			70		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (25°C) V_{GE} = 15V V_{Bus} = 600V I_C = 25A R_G = 22Ω		60		ns
T_r	Rise Time			50		
$T_{d(off)}$	Turn-off Delay Time			305		
T_f	Fall Time			30		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (125°C) V_{GE} = 15V V_{Bus} = 600V I_C = 25A R_G = 22Ω		60		ns
T_r	Rise Time			50		
$T_{d(off)}$	Turn-off Delay Time			346		
T_f	Fall Time			40		
E_{on}	Turn-on Switching Energy	V_{GE} = 15V V_{Bus} = 600V I_C = 25A R_G = 22Ω	T_j = 125°C	3.5		mJ
E_{off}	Turn-off Switching Energy		T_j = 125°C	1.5		

Reverse diode ratings and characteristics

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			1200			V
I_{RM}	Maximum Reverse Leakage Current	V_R=1200V	T_j = 25°C			100	µA
			T_j = 125°C			500	
I_F	DC Forward Current		Tc = 80°C		30		A
V_F	Diode Forward Voltage	I_F = 30A			2.6	3.1	V
		I_F = 60A			3.2		
		I_F = 30A	T_j = 125°C		1.8		
t_{rr}	Reverse Recovery Time	I_F = 30A V_R = 800V di/dt =200A/µs	T_j = 25°C		300		ns
			T_j = 125°C		380		
Q_{rr}	Reverse Recovery Charge		T_j = 25°C		360		nC
			T_j = 125°C		1700		

Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information).

Symbol	Characteristic	Min	Typ	Max	Unit
R_{25}	Resistance @ 25°C		50		kΩ
$B_{25/85}$	T_{25} = 298.15 K		3952		K

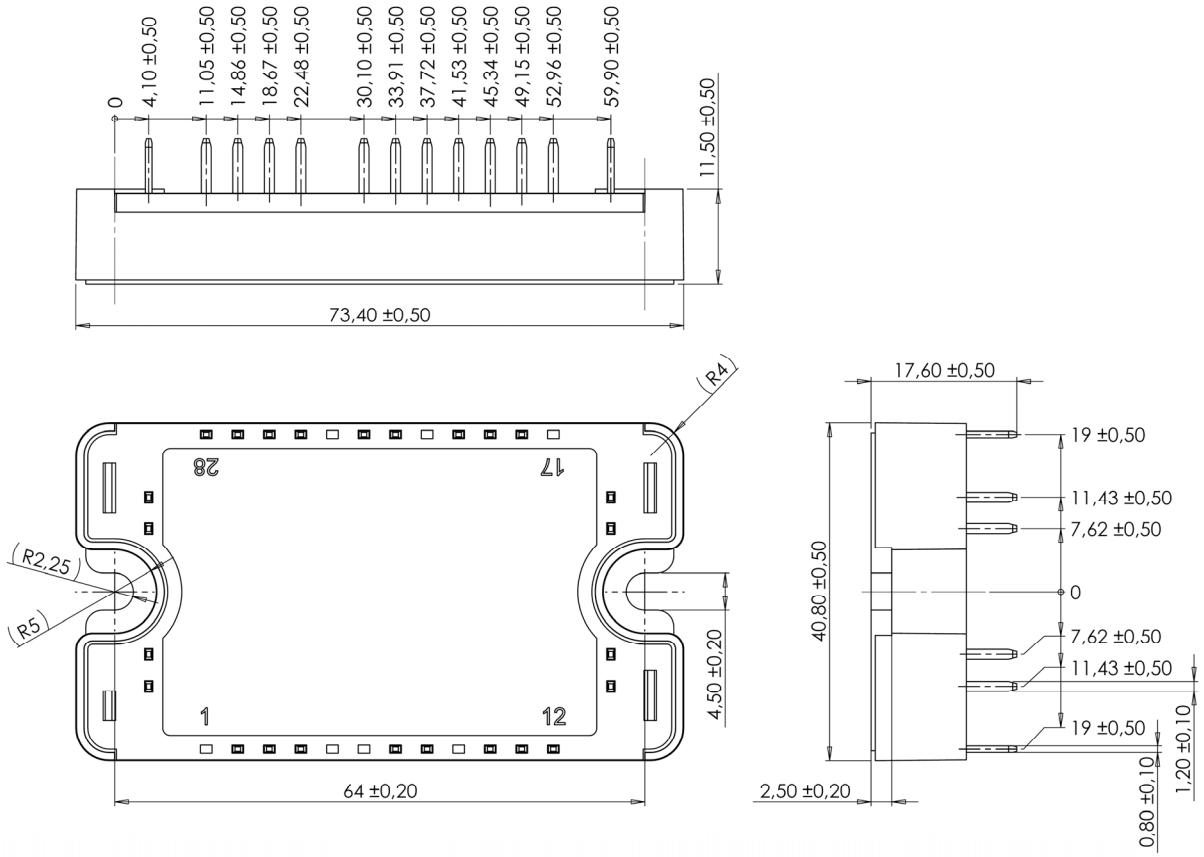
$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

T: Thermistor temperature
R_T: Thermistor value at T

Thermal and package characteristics

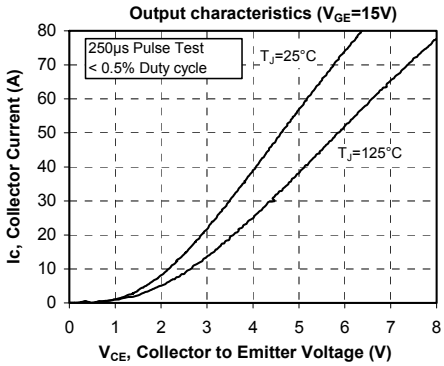
Symbol	Characteristic			Min	Typ	Max	Unit
R_{thJC}	Junction to Case Thermal Resistance		IGBT			0.6	°C/W
			Diode			1.2	
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T_J	Operating junction temperature range			-40		150	°C
T_{STG}	Storage Temperature Range			-40		125	
T_C	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					110	g

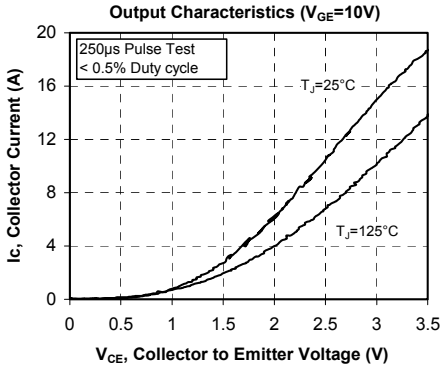
SP3 Package outline (dimensions in mm)

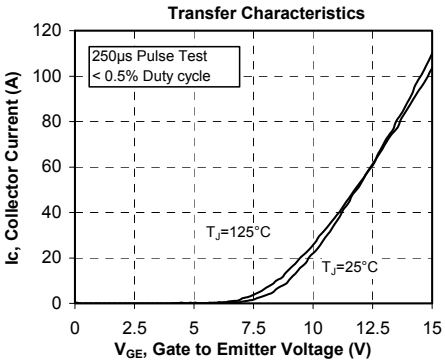


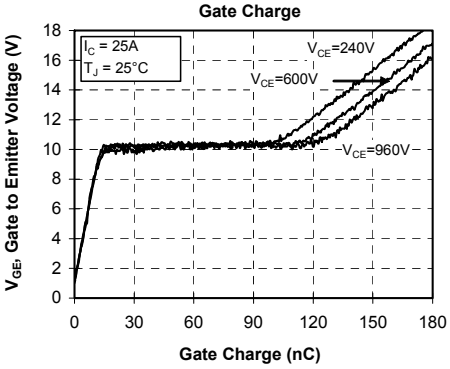
See application note 1901 - Mounting Instructions for SP3 Power Modules on www.microsemi.com

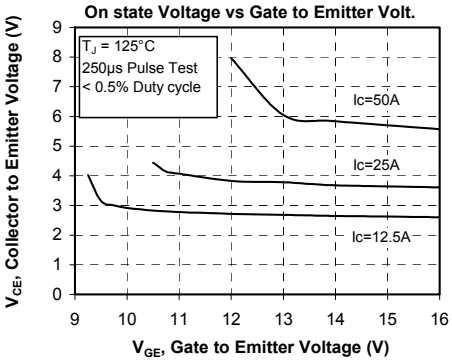
Typical Performance Curve

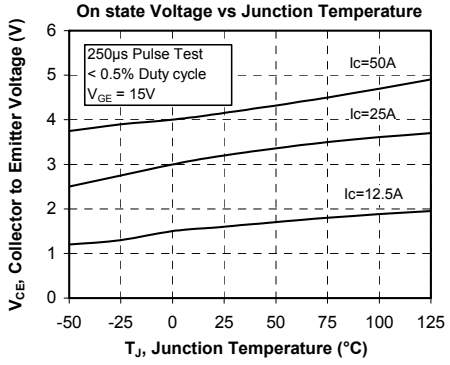


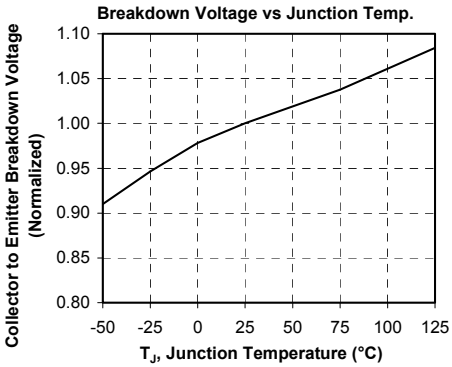


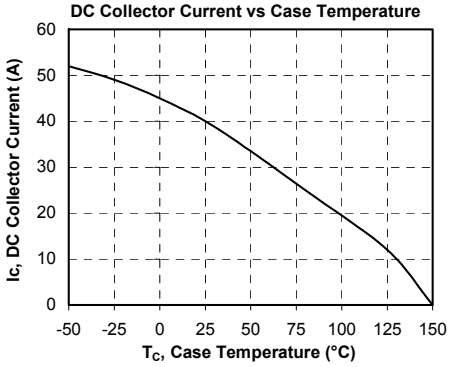




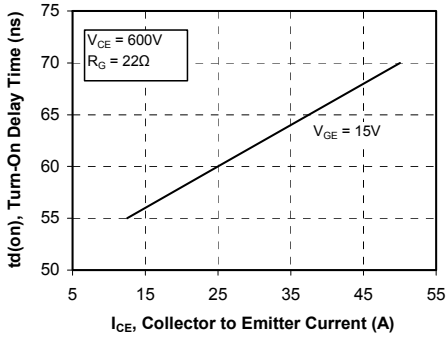




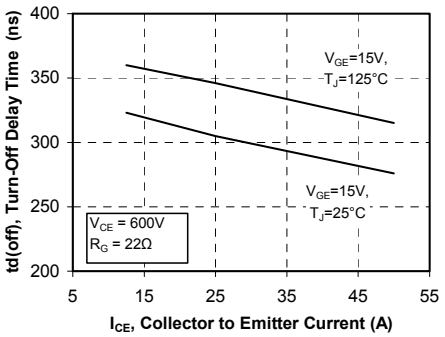




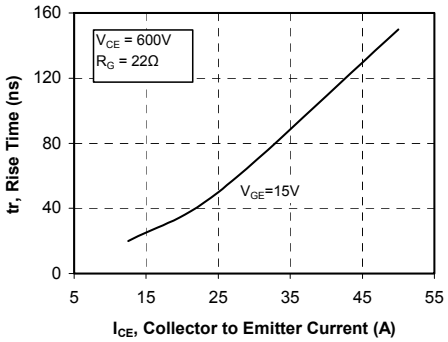
Turn-On Delay Time vs Collector Current



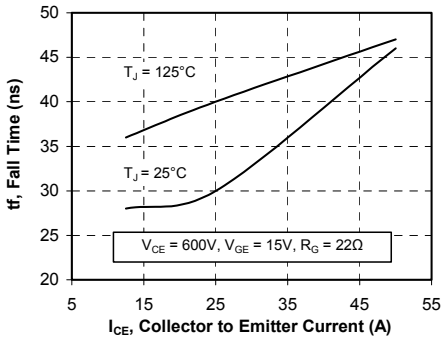
Turn-Off Delay Time vs Collector Current



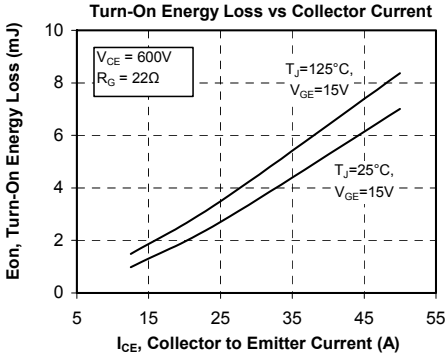
Current Rise Time vs Collector Current



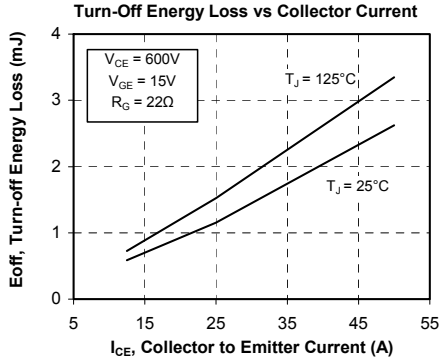
Current Fall Time vs Collector Current



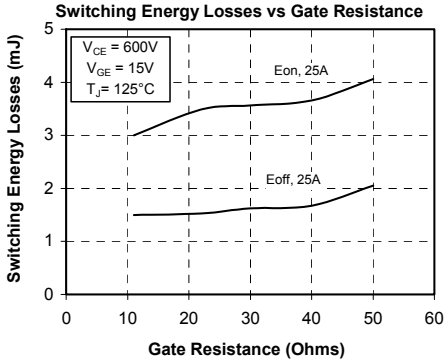
Turn-On Energy Loss vs Collector Current



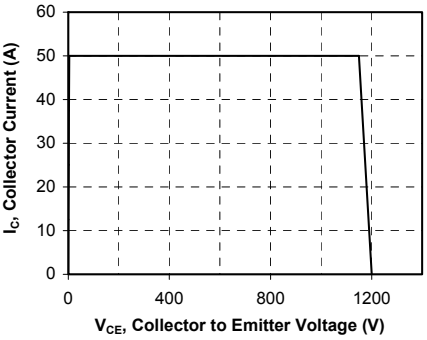
Turn-Off Energy Loss vs Collector Current

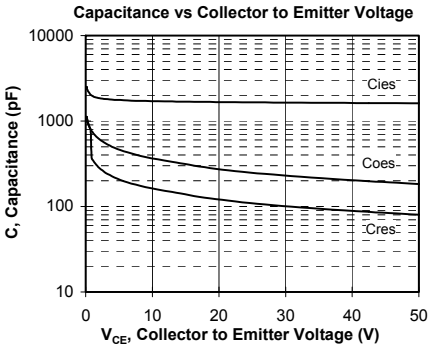


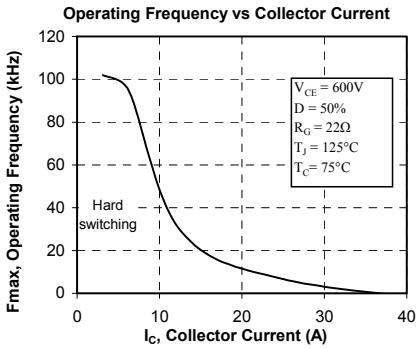
Switching Energy Losses vs Gate Resistance

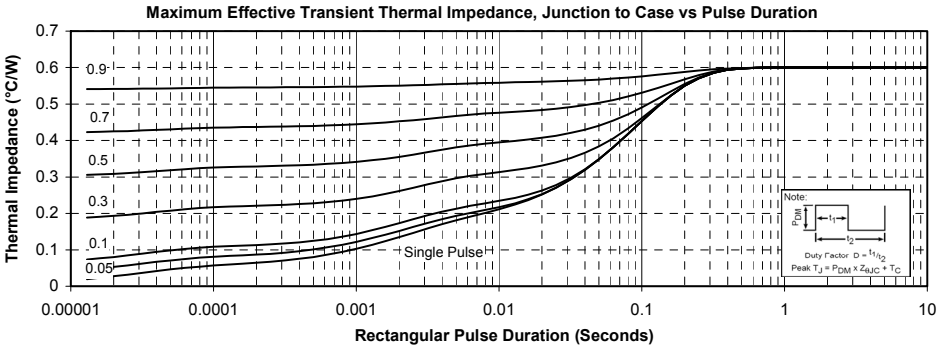


Reverse Bias Safe Operating Area




Capacitance vs Collector to Emitter Voltage
C, Capacitance (pF)
10000
1000
100
10
0
10
20
30
40
50
Cies
Coes
Cres
VCE, Collector to Emitter Voltage (V)


Operating Frequency vs Collector Current
Fmax, Operating Frequency (kHz)
120
100
80
60
40
20
0
0
10
20
30
40
VCE = 600V
D = 50%
RG = 22Ω
TJ = 125°C
TC = 75°C
Hard
switching
IC, Collector Current (A)


Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration
Thermal Impedance (°C/W)
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0.9
0.7
0.5
0.3
0.1
0.05
Single Pulse
Note:
PDM
t1
t2
Duty Factor D = t1/t2
Peak TJ = PDM x ZθJC + TC
0.00001
0.0001
0.001
0.01
0.1
1
10
Rectangular Pulse Duration (Seconds)

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